The OFDM transmitter and receiver cores have been targeted to the ZCU111 RFSoC development board.

MATLAB 2020a and Vivado 2019.1.

**Hardware statistics for transmitter**:

|  |  |  |  |
| --- | --- | --- | --- |
| **Resource** | **Used** | **Available** | **% Utilised** |
| LUT | 2,629 | 425,280 | 0.62 |
| FF | 3,832 | 850,560 | 0.45 |
| BRAM | 5 | 1,080 | 0.46 |
| DSP48 | 6 | 4,272 | 0.14 |

Clock frequency greater than 256MHz.

**Hardware statistics for receiver**:

|  |  |  |  |
| --- | --- | --- | --- |
| **Resource** | **Used** | **Available** | **% Utilised** |
| LUT | 10,673 | 425,280 | 2.51 |
| FF | 15,520 | 850,560 | 1.82 |
| BRAM | 0.5 | 1,080 | 0.05 |
| DSP48 | 80 | 4,272 | 1.87 |

Clock frequency greater than 256MHz.